

# Lecture 14

# FULL-WAVE ANALYSIS AND EQUIVALENT-CIRCUIT MODELING OF SIW COMPONENTS



- Introduction on Substrate Integrated Waveguide (SIW) components
- Basic concepts of the Boundary Integral-Resonant Mode Expansion (BI-RME) method
- BI-RME modeling of SIW interconnects
- BI-RME modeling of SIW components
- Effect of losses
- Equivalent circuit models of SIW discontinuities
- Conclusions

### OUTLINE



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### **SUBSTRATE INTEGRATED WAVEGUIDE**



Substrate Integrated Waveguides (**SIW**) are novel transmission lines that implement rectangular waveguides in planar form.



SIW consist of two rows of conducting cylinders embedded in a dielectric substrate that connect two parallel metal plates.

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SIW technology permits to realize waveguide components in a dielectric substrate by replacing the metallic side walls by arrays of metal vias.



### **EXAMPLES OF SIW COMPONENTS**





#### SIW post filter at 27 GHZ





#### SIW circulator at 24 GHz

#### SIW diplexer at 26 GHz



SIW hybrid coupler at 94 GHz

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### **EXAMPLES OF SIW ANTENNAS**



There are two major topologies of SIW antennas:

- slotted waveguide antennas are based on longitudinal slots;
- leaky-wave SIW antennas, obtained by properly spacing the metal vias in order to create radiation leakage.



# SYSTEMS-ON-SUBSTRATE (SOS)



Replacing the current System-in-Package (SiP) approach with the System-on-Substrate (SoS) concept for mm-wave systems.



Z. Li and K. Wu, "24-GHz Frequency-Modulation Continuous-Wave Radar Front-End System-on-Substrate," *IEEE Trans. on Microwave Theory and Techniques*, Vol. 56, No. 2, pp. 278-285, Feb. 2008.

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complete circuit in planar form (including

passive components, active elements and antennas)

low-cost and well-developed

manufacturing process (PCB, LTCC, ...)

high-density integration of

mm-wave components & systems

complete shielding (no interference) and

low losses (energy saving!)

### **EVOLUTION OF RESEARCH ON SIW**



#### There is a growing interest for SIW technology worldwide.



#### Number of publications on the SIW in IEEE journals (source: ieeexplore.ieee.org)





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#### BI-RME = Boundary Integral-Resonant Mode Expansion

# Originally **developed at the University of Pavia** for the modeling of waveguide circuits:



G. Conciauro, "The BI-RME method," Chap. 5 in: G. Conciauro, M. Guglielmi, and R. Sorrentino, *Advanced Modal Analysis*, New York, Wiley, 2000.

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The BI-RME method yields the admittance matrix *Y* of a **lossless** and **shielded** waveguide component in the form of a pole expansion in the frequency domain:

$$Y_{ij} = \frac{1}{j\omega} A_{ij} + j\omega B_{ij} + j\omega^3 \sum_{m=1}^{M} \frac{C_{im}C_{jm}}{\omega_m^2(\omega_m^2 - \omega^2)}$$
  
How-frequency terms modes of the cavity

where A, B, C,  $\omega$  are frequency-independent matrices obtained by solving a single real eigenvalue problem.



**Advantages** of the BI-RME method:

Modeling of components with arbitrarily shaped geometry

 Mathematical model of the component in the form of the pole expansion of the Y-parameters

Very fast!



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The **modal field propagation** in SIW interconnects is similar to classical rectangular waveguides (TE<sub> $n_0$ </sub> modes, n=1, 2, ...).



# EQUIVALENT WAVEGUIDE

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It is possible to define an **equivalent rectangular waveguide**, whose mode spectrum coincides with the SIW mode spectrum.



$$w_{\rm eff} = w - \frac{d^2}{0.95s}$$

EQUIVALENT WIDTH

### EQUIVALENT WAVEGUIDE



Once the equivalent width  $w_{eff}$  has been determined:



**Example** – Propagation constant versus frequency for the fundamental mode of an SIW ( $w = 10 \text{ mm}, d = 1 \text{ mm}, s = 2 \text{ mm}, h = 1 \text{ mm}, \varepsilon_r = 2.2$ ).

# Full-wave modeling by using **commercial software** (e.g. HFSS)

The analysis of the unit cell provides, for each phase shift  $\varphi$ , the eigenfrequencies f and the quality factors Q.

 $\beta = \frac{\Psi}{S}$ propagation constant

attenuation constant

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#### Full-wave modeling by the **BI-RME method**.

The unit cell of a 1D periodic structure is considered.



#### **Preliminary hypotheses** are needed to apply the BI-RME method:

- Iossless dielectric material and perfect conductor
- no radiation (addition of fictitious metallic side walls)
- embedding rectangular ports with modal vector  $\mathbf{E}_{WG}$ .

# **MODELING OF SIW INTERCONNECTS**



port 2

fictitious

metal wall

top ground plane

The modal propagation constants can be obtained by formulating an **eigenvalue problem** for the unit cell.

The *ABCD* matrix is derived from the *Y* matrix.

unit cell

metal vias

port 1

fictitious metal wall

$$\begin{bmatrix} ABCD_{11} & ABCD_{12} \\ ABCD_{21} & ABCD_{22} \end{bmatrix} \begin{bmatrix} V_2 \\ -I_2 \end{bmatrix} = e^{j\beta s} \begin{bmatrix} V_2 \\ -I_2 \end{bmatrix}$$

The eigenvalues at each frequency permit to determine the **propagation constant** of the modes.

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# MODELING OF SIW INTERCONNECTS



The SIW modal vectors  $\mathbf{E}_{SIW}$  are a metal vias linear combination of the rectangular port 2 fictitious waveguide modal vectors  $\mathbf{E}_{WG}$ : metal wall  $\mathbf{E}_{\mathrm{SIW}} = \mathbf{T} \mathbf{E}_{\mathrm{WG}}$ top ground plane fictitious port 1 metal wall unit cell  $\mathbf{T} = \begin{pmatrix} V_{11} & \dots & V_{1P} & 0 & \dots & 0 \\ \vdots & \ddots & \vdots & \vdots & \ddots & \vdots \\ V_{N1} & \dots & V_{NP} & 0 & \dots & 0 \\ \hline 0 & \dots & 0 & V_{11} & \dots & V_{1P} \\ \vdots & \ddots & \vdots & \vdots & \ddots & \vdots \\ 0 & \dots & 0 & V_{N1} & \dots & V_{NP} \end{pmatrix}$ *P*-th eigenvector

# **EXAMPLE: SIW INTERCONNECT**





Y. Cassivi, L. Perregrini, P. Arcioni, M. Bressan, K. Wu, G. Conciauro, "Dispersion Characteristics of Substrate Integrated Rectangular Waveguide," *IEEE Microwave and Wireless Components Letters*, Vol. 12, No. 9, pp. 333-335, Sept. 2002.



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# MODELING OF SIW COMPONENTS



The BI-RME modeling of SIW components requires the calculation of the generalized admittance matrix Y (lossless case, rectangular ports).



#### SIW circuits are H-plane waveguide components.

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# **MODELING OF SIW COMPONENTS**



The BI-RME method provides the Y matrix of SIW components referred to rectangular ports.

The Y' matrix referred to the SIW modes can be obtained as:





#### The resulting $\mathbf{Y'}$ matrix is still in the form of a pole expansion.





D. Deslandes and Ke Wu, "Single-Substrate Integration Technique of Planar Circuits and Waveguide Filters," *IEEE Trans. on Microwave Theory and Techniques*, Vol. MTT-51, No. 2, pp. 593-596, Feb. 2003.



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# LOSSES IN SIW STRUCTURES





M. Bozzi, M. Pasian, L. Perregrini, and K. Wu, "On the Losses in Substrate Integrated Waveguides and Cavities," *International Journal Microwave and Wireless Technologies*, 2009.

# **CONDUCTOR & DIELECTRIC LOSSES**



The BI-RME method can be modified to include **conductor** and **dielectric losses** by using a perturbation approach.

The *Y* matrix is written as a pole expansion in the frequency domain:



where the quality factor is defined as:

$$\frac{1}{Q_p} = \frac{1}{Q_p^{(c)}} + \frac{1}{Q_p^{(d)}} = \underbrace{\frac{1}{\sqrt{2\omega_p\mu_0\sigma_c}}\int_{S} \left|\mathbf{H}_p\right|^2 dS}_{\text{conductor loss}} + \underbrace{\frac{\sigma_d}{\omega_p\varepsilon_0\varepsilon_r}}_{\text{dielectric loss}}$$



After some algebraic manipulations, the final expression results:

$$Y_{ij}(k_{0}) = \frac{A_{ij}}{j\eta_{0}k_{0}} + \sigma_{d}B_{ij} + \frac{jk_{0}\varepsilon_{r}}{\eta_{0}}B_{ij} + \frac{k_{0}^{2}\varepsilon_{r}^{3/2}}{\eta_{0}}\sum_{p=1}^{P}\frac{C_{ip}C_{ip}}{k_{p}Q_{p}(k_{p}^{2} + jk_{0}k_{p}\varepsilon_{r}^{1/2}/Q_{p} - k_{0}^{2}\varepsilon_{r})} + \frac{jk_{0}^{3}\varepsilon_{r}^{2}}{\eta_{0}}\sum_{p=1}^{P}\frac{C_{ip}C_{ip}}{k_{p}^{2}(k_{p}^{2} + jk_{0}k_{p}\varepsilon_{r}^{1/2}/Q_{p} - k_{0}^{2}\varepsilon_{r})}$$

M. Bozzi, L. Perregrini, and K. Wu, "Modeling of Conductor, Dielectric and Radiation Losses in Substrate Integrated Waveguide by the Boundary Integral-Resonant Mode Expansion Method," *IEEE Trans. Microwave Theory & Techniques*, Vol. MTT-56, No. 12, pp. 3153-3161, Dec. 2008.



The **radiation leakage** is accounted for by considering additional side ports, terminated with matched loads.





#### Schematic of the circuit, with definition of the ports.



M. Bozzi, L. Perregrini, and K. Wu, "Modeling of Conductor, Dielectric and Radiation Losses in Substrate Integrated Waveguide by the Boundary Integral-Resonant Mode Expansion Method," *IEEE Trans. Microwave Theory & Techniques*, Vol. MTT-56, No. 12, pp. 3153-3161, Dec. 2008.

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# **EXAMPLE: SIW INTERCONNECT**





D. Deslandes and K. Wu, "Accurate Modeling, Wave Mechanisms, and Design Considerations of a Substrate Integrated Waveguide," *IEEE Trans. on Microwave Theory and Techniques*, Vol. 54, No. 6, pp. 2516–2526, June 2006.

# EXAMPLE: SIW WITH RADIATION





F. Xu and K. Wu, "Guided-Wave and Leakage Characteristics of Substrate Integrated Waveguide," *IEEE Trans. on Microwave Theory and Techniques*, Vol. 53, No. 1, pp. 66-73, Jan. 2005.

# EXAMPLE: SIW FILTER



#### SIW filter with three centered posts





w = 21.06 mm d = 2 mm s = 4 mm  $d_1 = 2 \text{ mm}$   $d_2 = 0.5 \text{ mm}$   $s_1 = 14 \text{ mm}$  h = 1 mm  $\varepsilon_r = 2$   $\sigma_d = 0.001 \text{ S/m}$  $\sigma_c = 4.10^7 \text{ S/m}$ 



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# MODELING OF COMPLEX CIRCUITS



The **full-wave analysis** of complicated SIW components and circuits can be performed using existing numerical codes, but their optimization may be prohibitively time-consuming.



However, in most cases, a circuit consists of a cascade of closely spaced simple discontinuities.

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# LIBRARY OF EQUIVALENT MODELS



Multi-mode parametric circuit models of simple discontinuities would permit the synthesis of SIW components by using commercial CAD tools.



M. Bozzi, L. Perregrini, K. Wu, "A Novel Technique for the Direct Determination of Multi-mode Equivalent Circuit Models for Substrate Integrated Waveguide Discontinuities," *Inter. Journal RF Microwave Computer–Aided Engineering*, July 2009.



The modeling of SIW components by the BI-RME method permits to derive the **equivalent circuit models**.

In the **lossless case**, the BI-RME method yields the admittance matrix in the form:

$$Y_{ij} = \frac{1}{j\omega}A_{ij} + j\omega B_{ij} + j\omega^3 \sum_{m=1}^{M} \frac{C_{im}C_{jm}}{\omega_m^2(\omega_m^2 - \omega^2)}$$



By extracting from the summation the term for  $\omega \rightarrow \infty$  we have:

$$Y_{ij} = \frac{1}{j\omega}A_{ij} + j\omega\left(B_{ij} - \sum_{m=1}^{M}\frac{C_{im}C_{jm}}{\omega_m^2}\right) + j\omega\sum_{m=1}^{M}\frac{C_{im}C_{jm}}{(\omega_m^2 - \omega^2)}$$

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# **EQUIVALENT CIRCUIT MODELS**

This expression permits a **direct identification of the equivalent circuit model** (no initial guess and fitting procedure!)

$$Y_{ij} = \frac{1}{j\omega} A_{ij} + j\omega \left( B_{ij} - \sum_{m=1}^{M} \frac{C_{im}C_{jm}}{\omega_m^2} \right) + j\omega \sum_{m=1}^{M} \frac{C_{im}C_{jm}}{(\omega_m^2 - \omega^2)}$$

$$L_0^{-1} \qquad LC \text{ series}$$



M. Bozzi, L. Perregrini, K. Wu, "Direct Determination of Multi-mode Equivalent Circuit Models for Discontinuities in Substrate Integrated Waveguide Technology," *International Microwave Symposium* (IMS2006), San Francisco, California, 2006.

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In the **lossy case**, the BI-RME method yields the *Y* matrix in the form:

$$Y_{ij}(k_{0}) = \frac{A_{ij}}{j\eta_{0}k_{0}} + \sigma_{d}B_{ij} + \frac{jk_{0}\varepsilon_{r}}{\eta_{0}}B_{ij} + \frac{k_{0}^{2}\varepsilon_{r}^{3/2}}{\eta_{0}}\sum_{p=1}^{P}\frac{C_{ip}C_{ip}}{k_{p}Q_{p}(k_{p}^{2} + jk_{0}k_{p}\varepsilon_{r}^{1/2}/Q_{p} - k_{0}^{2}\varepsilon_{r})} + \frac{jk_{0}^{3}\varepsilon_{r}^{2}}{\eta_{0}}\sum_{p=1}^{P}\frac{C_{ip}C_{ip}}{k_{p}^{2}(k_{p}^{2} + jk_{0}k_{p}\varepsilon_{r}^{1/2}/Q_{p} - k_{0}^{2}\varepsilon_{r})}$$

#### The expression of the *Y* matrix can be recast in the form:

$$Y_{ij}(\omega) = \frac{1}{j\omega} \frac{A_{ij}}{\mu_0} + \sigma_d B_{ij} + j\omega \left[ \varepsilon_0 \varepsilon_r B_{ij} - \sum_{p=1}^{P} \frac{C_{ip} C_{ip}}{\mu_0 \omega_p^2} \right] + j\omega \frac{1}{\mu_0} \sum_{p=1}^{P} \frac{C_{ip} C_{ip}}{\omega_p^2 + j\omega \omega_p / Q_p - \omega^2}$$

### **EQUIVALENT CIRCUIT MODELS**

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This expression permits a direct identification of the equivalent circuit:

$$Y_{ij}(\omega) = \frac{1}{j\omega} \frac{A_{ij}}{\mu_0} + \sigma_d B_{ij} + j\omega \left[ \epsilon_0 \epsilon_r B_{ij} - \sum_{p=1}^{p} \frac{C_{ip} C_{ip}}{\mu_0 \omega_p^2} \right] + j\omega \frac{1}{\mu_0} \sum_{p=1}^{p} \frac{C_{ip} C_{ip}}{\omega_p^2 + j\omega \omega_p / Q_p - \omega^2}$$

$$R_0 = \frac{1}{\sigma_d B_{ij}} \qquad L_0 = \frac{\mu_0}{A_{ij}} \qquad C_0 = \epsilon_r \epsilon_0 B_{ij} - \sum_{p=1}^{p} \frac{C_{ip} C_{ip}}{\mu_0 \omega_p^2}$$

$$Y_{ij} = \left\{ \begin{array}{c} \mathsf{R}_0 \\ \mathsf{R}$$

M. Bozzi, L. Perregrini, K. Wu, "Modeling of Losses in Substrate Integrated Waveguide by Boundary Integral-Resonant Mode Expansion Method," 2008 IEEE MTT-S International Microwave Symposium (IMS2008), Atlanta, GA, 2008.

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## **EQUIVALENT CIRCUIT MODELS**

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A **pi-type equivalent circuit model** is adopted to represent the SIW discontinuities.



# EXAMPLE: SIW DISCONTINUITY











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# **PARAMETRIC CIRCUIT MODELS**





Multiple analyses by changing some geometrical dimensions allow for finding **parametric models** (polynomial interpolation is adopted).









## EXAMPLE: 4-POLE SIW FILTER



Measured input matching 17.5 dB, insertion loss 2.3 dB (9.70–10.30 GHz).

Simulation with **multi-mode lossless equivalent circuits** (3 odd modes). Fabricated and measured at CTTC, Spain.

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# EXAMPLE: 8-POLE SIW FILTER



Measured input matching 15.7 dB, insertion loss 4.0 dB (9.55–10.45 GHz).

Computing time for one analysis (in 300 frequency points): **1.28 sec** by using the equivalent circuits, **25 min** by using HFSS.

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